	Application No.	Applicant(s)
Notice of Allowability	10/600,800 Examiner	OHKAWA ET AL. Art Unit
·		
	Donald M. Lair	2858
The MAILING DATE of this communication apperature of the Communication appearance of the Co	(OR REMAINS) CLOSED in this a or other appropriate communicati IGHTS. This application is subjec	application. If not included on will be mailed in due course. THIS
1. This communication is responsive to		
2. The allowed claim(s) is/are <u>1-15</u> .		
3.   The drawings filed on 19 June 2003 are accepted by the Examiner.		
<ul> <li>4.  Acknowledgment is made of a claim for foreign priority unally All b)  Some* c)  None of the:</li> <li>1.  Certified copies of the priority documents have</li> <li>2.  Certified copies of the priority documents have</li> <li>3.  Copies of the certified copies of the priority documents have International Bureau (PCT Rule 17.2(a)).</li> <li>* Certified copies not received:</li> </ul>	e been received. e been received in Application No.	
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		ly complying with the requirements `
5. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.		
6. CORRECTED DRAWINGS (as "replacement sheets") must	st be submitted.	
(a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached		
1) 🔲 hereto or 2) 🔲 to Paper No./Mail Date		
(b) ☐ including changes required by the attached Examiner' Paper No./Mail Date	s Amendment / Comment or in the	e Office action of
Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in the state of the stat	.84(c)) should be written on the dra the header according to 37 CFR 1.12	wings in the front (not the back) of
DEPOSIT OF and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT	sit of BIOLOGICAL MATERIA	L must be submitted. Note the
Attachment(s)  1. ☐ Notice of References Cited (PTO-892)  2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)  3. ☑ Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date 12/08/03  4. ☐ Examiner's Comment Regarding Requirement for Deposit	6. ☐ Interview Summa Paper No./Mail [ 08), 7. ☐ Examiner's Amer	Date
of Biological Material	9.	. ,

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## **DETAILED ACTION**

## Allowable Subject Matter

- 1. Claims 1 15 are allowed.
- 2. The following is an examiner's statement of reasons for allowance:
- 3. In regards to Claim 1, the prior art of record fails to disclose a semiconductor characteristic evaluation apparatus comprising a plurality of measurement units, a measurement bus, a plurality of measurement pads a plurality of selection switches and a control circuit wherein a plurality of measured pattern of components are mounted on the plurality of measurement units, wherein the measurement bus is arranged above the plurality of measurement units and connected to the plurality of measured patterns, wherein the plurality of measurement pads are arranged in a region other than an arrangement region of the measurement units and wherein the selection switches select a measurement bus group configuring the measurement circuit system in the combination as claimed.
- 4. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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## Pertinent Prior Art

5. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

6. Shimizu et al. (US-6,727,723), Whetsel (US-6,727,722), Fleury et al. (US-6,512,392), Shimizu et al. (US-6,400,173), Whetsel (US-6,326,801) and Yamashita et al. (US-5,506,516) all disclose devices and methods of testing semiconductors using patterns, switches and a measurement bus but do not teach the specific arrangements and interconnections as claimed.

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## Conclusion

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Donald M. Lair whose telephone number is (571) 272-2232. The examiner can normally be reached on Monday - Friday, 8 AM - 4:30 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, N. Le can be reached on (571) 272-2233. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Donald M. Lair
Patent Examiner

Art Unit 2858

August 12, 2004

ANIANI DEB

ANJAN DEB PRIMARY EXAMINER